



Docket No.: 57454-1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Akira YAMADA : Confirmation Number: 5917
Serial No.: 09/874,239 : Group Art Unit: 2188
Filed: June 06, 2001 : Examiner: Thang H. Ho

#6/A
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04/15/04

For: SYNCHRONOUS SIGNAL PRODUCING CIRCUIT FOR CONTROLLING A DATA
READY SIGNAL INDICATIVE OF END ACCESS TO A SHARED MEMORY AND
THEREBY CONTROLLING SYNCHRONIZATION BETWEEN PROCESSOR AND
COPROCESSOR

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APR 13 2004

AMENDMENT

Technology Center 2100

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action of January 14, 2004, please amend the above-identified
application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this
paper.

Remarks begin on page 11 of this paper.